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## » Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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1. **SysteMorph: dynamic/online/adaptive system-level optimization for SoC**  
Yoshimatsu, N.; Yoshida, M.; Soga, T.; Shuto, M.; Tanoue, Y.; Fujii, Y.; Eshima, K.; Hayashidai, Murakami, K.;  
High Performance Computing and Grid in Asia Pacific Region, 2004. Proceedings. Seventh Inter Conference on  
20-22 July 2004 Page(s):442 - 447  
[AbstractPlus](#) | Full Text: [PDF\(413 KB\)](#) IEEE CNF
2. **Systems-level optimization of low-temperature thermoelectric waste heat recovery**  
Crane, D.T.; Jackson, G.S.;  
Energy Conversion Engineering Conference, 2002. IECEC '02. 2002 37th Intersociety  
29-31 July 2004 Page(s):583 - 591  
[AbstractPlus](#) | Full Text: [PDF\(873 KB\)](#) IEEE CNF
3. **System and architecture optimizations for low power MPEG-1 video decoding**  
Molloy, S.; Jain, R.;  
Low Power Electronics, 1994. Digest of Technical Papers., IEEE Symposium  
10-12 Oct. 1994 Page(s):26 - 27  
[AbstractPlus](#) | Full Text: [PDF\(192 KB\)](#) IEEE CNF
4. **System-level optimization of architectural performance under varying service demands**  
Mulcare, D.B.;  
Engineering of Computer-Based Systems, 1996. Proceedings., IEEE Symposium and Workshop  
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5. **System performance of multi-sector smart antenna base stations for WCDMA**  
Lee, J.; Arnott, R.;  
3G Mobile Communication Technologies, 2001. Second International Conference on (Conf. Publ 477)  
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6. **Battery modeling for energy aware system design**  
Rao, R.; Vrudhula, S.; Rakhmatov, D.N.;  
Computer  
Volume 36, Issue 12, Dec. 2003 Page(s):77 - 87  
[AbstractPlus](#) | References | Full Text: [PDF\(432 KB\)](#) IEEE JNL
7. **A fully integrated 0.13-/spl mu/m CMOS mixed-signal SoC for DVD player applications**

Okamoto, K.; Morie, T.; Yamamoto, A.; Nagano, K.; Sushihara, K.; Nakahira, H.; Horibe, R.; Aida, Takahashi, T.; Ochiai, M.; Soneda, A.; Kakiage, T.; Iwasaki, T.; Taniuchi, H.; Shibata, T.; Ochi, T; Takiguchi, M.; Yamamoto, T.; Seike, T.; Matsuzawa, A.;  
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[AbstractPlus](#) | [References](#) | Full Text: [PDF\(1564 KB\)](#) IEEE JNL

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Wassal, A.G.; Hasan, M.A.;  
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on  
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[AbstractPlus](#) | [References](#) | Full Text: [PDF\(632 KB\)](#) IEEE JNL

**9. Electromagnetic modeling and harmonic analysis of micro-machined ring-type magnetic f sensor using SPICE**

Gupta, S.; Ahn, C.H.; Park, J., II;  
Sensors, 2003. Proceedings of IEEE  
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[AbstractPlus](#) | Full Text: [PDF\(305 KB\)](#) IEEE CNF

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Stammermann, A.; Kruse, L.; Nebel, W.; Pratsch, A.; Schmidt, E.; Schulte, M.; Schulz, A.;  
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Ruha, A.; Kostamovaara, J.;  
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Chandrakasan, A.P.; Allmon, R.; Stratakos, A.; Brodersen, R.W.;  
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Cofer, R.H.; Sanders, T.J.; Means, D.P.;  
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Abedinpour, S.; Shenai, K.;  
Devices, Circuits and Systems, 2000. Proceedings of the 2000 Third IEEE International Caracas  
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**15. Energy efficient modulation and MAC for asymmetric RF microsensor systems**

Wang, A.Y.; SeongHwan Cho; Sodini, C.G.; Chandrakasan, A.P.;  
Low Power Electronics and Design, International Symposium on, 2001.  
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**16. Optimization of a retargetable functional simulator for embedded processors**

Papariello, F.; Luculli, G.;  
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- 17. Teaching trade-offs in system-level design methodologies**  
Sakiyama, K.; Schaumont, P.; Hwang, D.; Verbauwheide, I.;  
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- 18. Selective optimization of locks by runtime statistics and just-in-time compilation**  
Odaira, R.; Hiraki, K.;  
Parallel and Distributed Processing Symposium, 2003. Proceedings. International  
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- 19. Mica: a wireless platform for deeply embedded networks**  
Hill, J.L.; Culler, D.E.;  
Micro, IEEE  
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[AbstractPlus](#) | [References](#) | Full Text: [PDF\(553 KB\)](#) IEEE JNL
- 20. Proceedings of International Conference on Computer Aided Design**  
Parallel and Distributed Processing, 1996. Eighth IEEE Symposium on  
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- 21. Trends in low power digital signal processing**  
Chandrakasan, A.; Amiritharajah, R.; Goodman, J.; Rabiner, W.;  
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Volume 4, 31 May-3 June 1998 Page(s):604 - 607 vol.4  
[AbstractPlus](#) | Full Text: [PDF\(388 KB\)](#) IEEE CNF
- 22. Design and optimization of networked wireless information systems**  
Srivastava, M.;  
VLSI '98. System Level Design. Proceedings. IEEE Computer Society Workshop on  
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- 23. Simulation of sigma-delta data converters using SPICE**  
Kershaw, S.M.; Sandler, M.B.;  
Advanced A-D and D-A Conversion Techniques and Applications, IEE Colloquium on  
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### Early analysis tools for system-on-a-chip design

This special **component** is connected into the design in place of chip input ports.

... sets the stage for a new family of **system-level optimization** tools. ...

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**component** is connected into the design in place of chip. input ports. ...

new family of **system-level optimization** tools. Such tools ...

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### [PDF] Holistic QoS

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At a **system level**, **optimization** experts create a view of the resource ...

aggregation of **component** QoS Definitions would describe the QoS of the whole ...

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Local variables treated at **system level optimization**. – Robust wrt optimization

algorithms ... Summary: MDO Synthesis: One-of-a-Kind vs. Modular, **Reusable** ...

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### [PDF] CONSTRAINT-GUIDED SELF-ADAPTATION

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This, in effect, implies elimination of **component** and system design alternatives

... Mode-level optimization does not imply **system-level optimization** as the ...

[www.isis.vanderbilt.edu/publications/archive/Neema\\_S\\_0\\_0\\_2003\\_Constraint.pdf](http://www.isis.vanderbilt.edu/publications/archive/Neema_S_0_0_2003_Constraint.pdf) - [Similar pages](#)

### [PPT] NEST PI Presentation Template

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... and automatic generation and **system-level optimization** of the middleware

layer (CCSL) ... Graphical environment for TinyOS **component** interface checking ...

[www.isis.vanderbilt.edu/projects/nest/documentation/Vanderbilt\\_NEST\\_SanFrancisco.ppt](http://www.isis.vanderbilt.edu/projects/nest/documentation/Vanderbilt_NEST_SanFrancisco.ppt) - [Similar pages](#)

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### [PPT] Faculty recruitment talk

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**Component** Middleware for Distributed Real-time & Embedded (DRE) Systems ...

Middleware factors out many **reusable** mechanisms & services from what was ...

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### [PDF] An Analysis-Based Approach to Composition of Distributed Embedded ...

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of a mix of mostly **reusable** components and some application. specific components.

Currently, this **component-based** design approach works as ...

[www.cs.washington.edu/research/lis/papers/pdf/chou-codes98.pdf](http://www.cs.washington.edu/research/lis/papers/pdf/chou-codes98.pdf) - [Similar pages](#)

### Nat' Academies Press, Evaluation of the National Aerospace ...

Robust, **reusable** access to space will require a phased flight-test program ...

**Component** and subsystem development leading to space launch capability faces ...

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## [\[PDF\] Asteroid Rescue Mission](#)

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- large number of function calls typical of **system level optimization**, ... heavy lift expendable, and a high flight rate **reusable** launch vehicle (RLV). ...

[www.lpi.usra.edu/publications/reports/CB-1106/gatech01.pdf](http://www.lpi.usra.edu/publications/reports/CB-1106/gatech01.pdf) - [Similar pages](#)

## [\[PDF\] Exergy Methods for the Generic Analysis and Optimization of ...](#)

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supersonic **reusable** pilotless flight research program. ... optimization addressed the issue of **system level optimization** of high speed tactical weapons. ...

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## SUMMARY OVERVIEW

... waste or by-products lead to challenging **system level optimization** questions,

... Increase the amount of recyclable or **reusable** material in electronic ...

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## Sept 95 Newsletter

Applications: innovative uses of MCM technology, **system-level optimization**. ...

Impact of Packaging on **Component** and System Performance- \* FCC ...

[msrc.wvu.edu/cpmt/vol95.5.html](http://msrc.wvu.edu/cpmt/vol95.5.html) - 99k - [Cached](#) - [Similar pages](#)

## DAC 97: Abstracts

Session 15: **SYSTEM-LEVEL OPTIMIZATION AND VERIFICATION** ... The availability of completely designed and verified **reusable** design components has become a ...

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## Abstracts ICCAD 96

2B.2 Synthesis Of **Reusable** DSP Cores Based On Multiple Behaviors: W. Zhao, CA

Papachristou ... Session 11B : **SYSTEM LEVEL OPTIMIZATION AND VALIDATION** ...

[www.sigda.org/Archives/ProceedingArchives/](http://www.sigda.org/Archives/ProceedingArchives/)

[iccad/iccad96/papers/1996/iccad96/htmfiles/sun\\_sgi/iccadabst.htm](http://iccad/iccad96/papers/1996/iccad96/htmfiles/sun_sgi/iccadabst.htm) - 130k - [Cached](#) - [Similar pages](#)

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## [\[PDF\] Tuesday, June 10 Wednesday, June 11](#)

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**System-Level Optimization.** and Verification. Chair: Phil Duncan ... of integrating the **reusable** components? Whose problems are they? ...

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## [\[PDF\] Rigid Body Formation Control of a Multi-Agent Mobile ad hoc ...](#)

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ware resources is encapsulated into **reusable** objects. TinyOS does not rely on preemptive ... sensor architecture for **system-level optimization**. In ...

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FIRST GLIMPSE AT **SYSTEM LEVEL OPTIMIZATION** ... Li-Ion. **Reusable**. Alkaline. Li-. Polymer. Facture 4 over the last 10 years! ...

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## [\[PDF\] A Hierarchical, Automated Design Flow for Low-Power,](#)

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**system-level optimization** as a post-doc here at Berkeley. ... Conversely, we would like to make our dataflow graphs as **reusable** as possible. ...

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number of constraints to the **system level optimization**. problem. ... information and make it **reusable** in a robust fashion so ...

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Without integrated (and **reusable**) data bases, including archived and ... Miss any one **component** and we're building in inherent problems at the outset. ...

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... **System Level Optimization** and Design Space Exploration for Low Power ...

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... analyses on a **component** or an assembly of structural components. ... It separates the **system level optimization**, having a relatively small number of ...

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Aero thermal Test of Thermal Protection Systems for X-33 **Reusable** Launch Vehicle

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Over the twenty years of operation, the issues of **reusable** space launch have become

... Advances in **component** technology such as hydrostatic bearings for ...

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equality constraints, **system-level optimization** generally will encounter no-solution

... plicated multidisciplinary **reusable** launch-vehicle design with in- ...

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A new two-stage system with a **reusable** booster. was designed to meet the challenge —

Sirius. This ... generated, the **system-level optimization** process ...

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This type of rugged, **reusable** shipping container can be suitable for commercial

... Our design is based on a **system-level optimization** approach that ...

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Each **component** of traditional manufacturing system for piping and TCL will be

modified ... Mid? proposes to design a **reusable** device that will record these ...

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problem was referred to as the **Reusable Space Transportation System (RSTS) LSA.**

... Probabilistic uncertainty distributions of **component-level** weights are ...

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supersonic transport and a **reusable** launch vehicle to derive computing ...

B Complete the collaborative **system-level optimization** and assessment of the ...

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to eliminate the unwanted **component** of the acoustic field in a given area, ...

and optimization techniques for the conceptual design of a NASA **reusable** launch ...

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A sensor-side **component** running on the sensor nodes and ... ture for **system-level optimization**, Tech. report, UC Berkeley, 2001. [Hil03] ...

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5.1 Selected Energy Densities for Battery **Component** Combinations . . . . .

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... Phone 387-7370 Ronald E. Shaffer Joseph Salvo Timothy Sivavec Lloyd Salsman **Component** Biosciences Combichemistry and Characterization Technologies, Niskayuna ...

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Impact of **reusable** launch vehicles (RLV) and other unmanned air vehicles on ATM.

... However, propulsion system **component** failures are still a contributing ...

[sbir.gsfc.nasa.gov/SBIR/sbir99/phase1/solicitation/99sbir.txt](http://sbir.gsfc.nasa.gov/SBIR/sbir99/phase1/solicitation/99sbir.txt) - 463k - [Cached](#) - [Similar pages](#)

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... how to decompose the legacy application into several **reusable** components based ... Through attributes, each **component** can initialize the variables and return the ...

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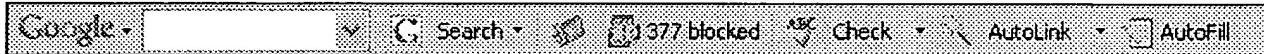
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sub-system level [SEI90]. It is believed that software **component** technologies can  
... and d[b[q]] are expressions (software **systems**), **component** b is reused. ...

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If the **components** in a **reusable library** are abstracted to the **code level**, ...

With a generator **optimization** can be performed on the input **system** ...

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interfaces and functions from a **reusable component** and ... **System-level**, design and  
**optimization** should drive the extraction, ...

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level of abstraction, the more frequently a **component** will be **reused**. ...

Designing **reusable components** for embedded real-time **systems** is even more ...

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**system-level IP reuse** methodology intended for soft and. firm IP blocks. ...

**System** buffers. (large P/A/T overhead). **Component**. 1. **Reusable IDCT component** ...

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[Automotive Industries: Standardization vs. optimization in ...](#)

For example, many internal **components** are **reused** across Toyota's family of ...

This will drive further standardization at the **system level** beyond just ...

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**Reusable components**. - Communication-based design ... L. Benini, G.

De Micheli, "System-Level Power Optimization: Techniques and Tools", ACM ...

[www.cyd.liu.se/users/~kamva881/subject/System%20Design%20and%20Methodology/Recommended\\_papers.pdf](http://www.cyd.liu.se/users/~kamva881/subject/System%20Design%20and%20Methodology/Recommended_papers.pdf) - [Similar pages](#)

[PDF] [Component Software & Technologies](#)

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Generations of **Component Models** for. Low-level **Systems**. Jay Lepreau, University  
of Utah. The OSKit is a collection of **reusable components** for building low- ...

[www.cs.brown.edu/industry/ipp/symposia/ipp28/brochure.pdf](http://www.cs.brown.edu/industry/ipp/symposia/ipp28/brochure.pdf) - [Similar pages](#)

[Penix - WISR9 Position Paper](#)

Interface adaptation determines how the **component** to be **reused** is bound to the  
problem ... Validating **component** compositions in software **system** generators. ...

[www.umcs.maine.edu/~ftp/wisr/wisr9/final-papers/Penix.html](http://www.umcs.maine.edu/~ftp/wisr/wisr9/final-papers/Penix.html) - 31k - Cached - Similar pages

## Compiler Optimization-Space Exploration

LSE consists of a user-extensible library of **reusable components**, ... the designer can use **component** parameters and **system** connectivity in one level of the ...

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behavioral level optimization in Table 2. In this case, the optimization process is composed of ... when the library of **reusable components** grows up, the ...

[www.imse.cnm.es/esd-msd/PUBLIC\\_DELIV/RAPID/ECCTD01\\_paper.pdf](http://www.imse.cnm.es/esd-msd/PUBLIC_DELIV/RAPID/ECCTD01_paper.pdf) - Similar pages

## Citations: Draco: A Method for Engineering Reusable Software ...

Ph.D. Proposal: High-level Optimization for Software Libraries - Guyer (Correct)

... of **reusable software components** which are appropriate for large **systems** ...

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## Citations: Generative Programming and Active Libraries - Czarnecki ...

... with **reusable components**, the focus could be moved from single **systems** to ...

Such libraries handle high level optimization themselves, leaving only low ...

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architecture ... Pipelining at **system-level**. Architectural optimization ...

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Knowledge from high-level behavioral model. Static optimization 2.4x energy ...

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Level. **Components**. Derived. **Components**. Conventional. **Components**. Reuse ...

Neighbors, Draco: A Method for Engineering Reusable Software Systems, ...

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respect to embedded **systems** by developing (I). a **Component Model** for embedded

**system** com- ... Black-box **component reuse** seems to be the best ...

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level of abstraction by composing the **system** with a mix of  **reusable**. and  **system-specific components**. Currently, these **components** as- ...

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## Printview - LowPower Competence Group - www.lowpower.de

At **system level** specifications of multimedia and communication **systems** ...

power consumption of  **reusable components** and in the area of analysis of **system** ...

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matically turn these circuits into **reusable components**. How well ... equations at the **system level** to help guide our **optimization** deci- sions. Fig. ...

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**ATR system level**. (structural models. of signal flows showing ATR. configurations, resource models ... compose **systems** at run-time from **reusable components** ...

[sunset.usc.edu/events/GSAW/gsaw99/pdf-presentations/session-7/salasin.pdf](http://sunset.usc.edu/events/GSAW/gsaw99/pdf-presentations/session-7/salasin.pdf) - [Similar pages](#)

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of off-the-shelf **system** parts, thus enabling better **reuse** of proven solutions.

... **cation level components** can be migrated (offloaded) between nodes, ...

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provides **reusable components** for building distributed applications. ... we coded the MedJava image filters using the **source level optimization** techniques ...

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**System-Level Design. MEMS Systems.** – Multiple **MEMS components**. – Associated circuits (on/off chip) ... Achieving one or more **optimization** goals by sweeping ...

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specific **components**, lack of documented **reusable** strategies at all levels of granularity, ... **systems** is to raise the **level of reuse** by providing **reusable** ...

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## ACM Computing Surveys: Towards Reusable, Extensible Components

Design patterns and **reusable components** share a number of properties, ...

for better performance and easier interoperability with low-level **systems** code. ...

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**Transmuting Raw Code into Reusable Components**. Jay Lepreau. (PI). Matthew Flatt

... **Alchemy**. **Component** language and tools. for low-level **systems** software ...

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As in the **System level** course the exercises will be a mix of critiquing existing

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is to lay foundations for libraries of **reusable components** and knowledge sharing

... Knowledge based **systems** (KBS) are often modeled by means of three con- ...

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... **systems** and platforms for Continuous Global Optimization implement specific and

... ffl Extraction and definition of generic and **reusable components**. ...

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verification of a generated netlist using a **system level** testbench. This is an

optional activity ... functions in design as macro-primitive **reuse**. **System** ...

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**reusable, components**. 1.0 Introduction. Major weapons **systems** as well as civilian manufacturing, ... simulation and **optimization** of complex **system** designs. ...

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## Mirabilis Design - Modeling - Technology Backgrounder

**System** failures occur in the interaction of **components** (hardware, mechanical and

... analysis and **optimization** at the **system-level** before implementation. ...

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resources to customize **components** into **reusable** solutions. "ABB's technologies

are rapidly ... The top **level**, **Optimization**, will enable multiple devices to ...

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Software Engineering with **Reusable components**. Springer, 1997. T125M211 **Optimization** of Control Algorithms of Technological Processes ...

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exploit **reusable component** libraries are well positioned to best satisfy the ...

**System** performance is equally dependent on the topologies or **component** ...

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At a **system level**, **optimization** experts create a view of the resource ... In a

component-based TP **system** where designers and **optimization** experts try not to ...

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As the complexity and integration level of **reusable components** increase, ...

**optimization** and synthesis of the entire **system**, and to give ...

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**Performance Optimization.** To meet either **system** performance or constraint ...

Designs are **reusable** across multiple **systems**, independently of the specifics ...

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Clearly, the three biggest challenges are **system-level** design, timing and power

... Customers are demanding more focus on **reusable** intellectual property and ...

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these **systems** a need for **reusable components** arose in the **systems** development

... At the same time it provides a required level of detail and formalism that ...

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**System level.** exploration can be achieved through **optimization** on behav- ...

facilitate design exploration, IP **reuse** and integration of com- ...

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A **component** is a **reusable** software program that ... only then that the **optimization** at the **system level** becomes meaningful. **IMPLEMENTATION ISSUES** ...

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[Composing Reusable Software Components through Views](#)

The ability to combine separate **reusable** software **components** to form a complete

... Most **systems** of software **reuse** impose significant restrictions on the ...

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But **system** design has a long history, and qualification and **reuse** of IP is part of

... Simutech, in conjunction with the ISLI (Institute for **System Level** ...

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Allows you to create **reusable** software. **components**, which can be easily retargeted to

... the **optimization level** with sliders for code density, execution ...

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easily define the **optimization level** with sliders ... Allows you to create **reusable**

software. **components**, which can be easily retargeted to ...

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[The Three "R's" of Mature System Development: Reuse, Reengineering ...](#)

Moreover, common architecture facilitates design and **component reuse** as ...  
Architecture captures important high-level concepts in a **system** that must be ...  
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This is a state-of-the-art **optimization** tool that is Web linked ... Cell development at the **component** and vehicle **system level**. (as we did with HEVs) ...  
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The CoWare Multimedia Design Library is a **system-level** modeling and analysis ...  
Complex **system** examples built from the library **components** include digital ...  
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While SoC design and **Virtual Component (VC) reuse** are on their way to becoming ... The HFSM structure allows easy behavioral-level **optimization** through code ...  
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### HW/SW Codesign MEDEA/ESPRIT Conference

A simple example will show how this will enable high-level **system optimization**. ... Due to the fact that the **level** of complexity constantly increases, **reuse** ...  
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... is extended to integrate **system-level** and **component-level** design requirements.  
Large aerospace **systems** are too complex to study on a single level, ...  
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However, current implementations of **component-based systems** for ... (limiting **reuse**), from the lack of corresponding high-level development tools (eg, ...  
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## A system-level methodology for low power design

It should be noted that **system-level optimization** targets dynamic power ...

These models depend on the input data, **component** characteristics such as bit ...

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Six years ago the chip was still a **component** in the **system**, ... From the top down, scoping and **system-level** analysis tools can use these models to build ...

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## CommsDesign - ASICs vs. Net Processors: Understanding the True Costs

**Reuse; System** deployment; Support, maintenance and evolution of **systems** already

... Low-level NP software development and **optimization** is complex, lengthy, ...

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## CommsDesign - ASICs vs. Net Processors: Understanding the True Costs

Low-level NP software development and **optimization** is complex, lengthy, ...

At the **system-level**, this cost manifests itself in the form of lost sales, ...

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## Synopsys INTRO

AMPS® is the only automatic transistor-level **optimization** tool that ... at a higher **level** of abstraction, creating and integrating **reusable components** and ...

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and a set of **reusable components** that abstract the low **level** timing and ...

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## [PDF] Formalized three-layer system-level model and reuse methodology ...

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cause the ACTS handle **system-level** control through mode ... two **reusable components**: a bumper sensor and a timer pro-. cess Fig. 2) and Table 2. ...

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at this **level** will also enable him or her to **reuse components**. across applications and through ... ing **component systems** is that they don't support abstrac- ...

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... simultaneously achieve high design efficiency by **component reuse** (IP blocks) and

... The work will concentrate on the early design phases (**system level** ...

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**Optimization Algorithms for Design.** (component overview continued). • Objective

... Local variables treated at **system level optimization** ...

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### Intelligent Enterprise : Autonomic Computing (printable version)

He advocates starting at a high **level** and subsequently recursively analyzing ...

Their RISC-style database **system** would be built from **reusable components**, ...

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### [PDF] Decoupling Source Trees into Build-Level Components

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software **systems** from build-level **components**. ... to improve **reuse** practice because build-level **components** can be reused individually ...

[www.win.tue.nl/ipa/activities/springdays2005/Jonge1.pdf](http://www.win.tue.nl/ipa/activities/springdays2005/Jonge1.pdf) - [Similar pages](#)

### Advanced Technology Focused Program in Component Based Software

creation of systematically **reusable** software **components** ... Practical mechanisms for interoperation with legacy **systems** ...

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### Department of Computer Science and Engineering at Oakland University

The following is the complete list of 500-700 level courses offered by the ...

hierarchies for **reuse**, designing **reusable components** and libraries, ...

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### Component-Oriented Software Pitfalls and Opportunities by Jack ...

... be constructed from other **components** and so on down to the **level** of pre-built **components** or ... One opportunity is for **system** tuning and **optimization**. ...

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### Table 2.1 | Design in the New Millennium: Advanced Engineering ...

... **optimization** and automatic cascade to next lower **level** of design ... tracking and trade-offs of subsystems and **components** managed by **system** tracking ...

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### Preliminary Program

The design and **optimization** of **systems** with this **level** of complexity requires a

... and (iii) **reusable** libraries of modeling **components** to facilitate rapid ...

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### Programming with Code Warrior

CodeWarrior for 8- and 16-bit Embedded **Systems** is a powerful and easy-to-use ... of **reusable components** Full-chip Simulator from P&E Microcomputer **Systems** ...

[www.njqrp.org/hc908/codewarrior.html](http://www.njqrp.org/hc908/codewarrior.html) - 16k - [Cached](#) - [Similar pages](#)

### MicroPlatform

The peripheral is typically a **reusable component** that can be synthesized in silicon.

... Each **level** constitutes a unique configuration, or **system** view, ...

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### Forschung

In the second stage, **components** and services are assembled to a **system**. ...

I solve this problem with transformation and **optimization** techniques. ...

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### Erhu: Architectural-level Dependence Analysis and Its Applications

The software architecture of a **system** defines its high-level structure, ...

Second, architectural **reuse** should benefit from dependence analysis. ...

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### A hybrid approach for core-based system-level power modeling

A core is a **reusable system-level component**, such as a microprocessor, coprocessor or ... Recently, a behavioral-level power optimization method has been ...

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### Reasoning Systems, Inc. - Reengineering and Rewriting Legacy ...

Legacy Systems Consume High Percentage of Corporate Resources ... By creating **reusable components** and automated composition techniques for adapting ...

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descriptions, the **system** would enable reuse. of hardware **components** (at the behavioral. level). ... front-end to the HDL-based optimization. **system**. ...

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### CWSE08

The CodeWarrior IDE includes a state-of-art source-level debugger with a wide ... of **reusable components** Full-chip Simulator from P&E Microcomputer **Systems** ...

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### Engineering of Computer-Based Systems 1996

A New Method for Identification of **Reusable Software Components**. ... **System-level optimization** of architectural performance under varying service demands. ...

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### BI Journal - Article

... the best solution is one that's transparent and provides many **reusable components**.

Unlike lower-level reuse repositories that have failed to meet ...

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### High-Level Parallel Programming Models and Supportive Environments ...

However, current implementations of **component-based system** for ... (limiting reuse), from the lack of corresponding high-level development tools (eg, ...

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Reuse leverages other people's resources. By reusing existing **components**, ...

Reuse did work its way into chip-level design in a design methodology called ...

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... database of **reusable component** information from internal databases, ...

**System** designers using PCs and moving to high-level design tools now have access ...

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### Felix rounds up seven players for methodology game

But at the **system** level, there are equal challenges in the definition of features

... implementation of a **reusable component** or of a complete architecture, ...

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At the **system** integration level, the Model Coupling Toolkit2 (MCT), ... Finally, at the algorithmic level, MCT-derived **components** are being used to create a ...  
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implement reusable and highly configurable operating **systems**. ... The problem with this **reusable component** design is that C++ language features like dynamic ...  
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Formal methods for safe and trustworthy decomposable and **reusable components** ...  
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established at subsystem levels hold at the **system level** ... **Reusable Component Library**. Desired Pluggable. Common **Components**. Process. Embedded. Software ...  
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programme

... or of **reusable software components** which can be combined into a program. ...  
At the highest **level**, a **system** is a composed model, and individual ...  
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